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(57) Abstract :

The present technique relates to a data processing device that can improve tolerance of data errors and to a data processing method. When a predetermined LDPC code having a coded rate of 8/15 is mapped with a code length of 16200 bits over 256 signal points and when a demultiplexer designates the code bits of 8—1 bits and bits $i+1$ from the highest of the symbol bits of the 8—1 bits of one symbol as bits b_i and y_i respectively replacements are performed for allocating b_0 to y_2 b_1 to y_6 b_2 to y_1 b_3 to y_0 b_4 to y_7 b_5 to y_5 b_6 to y_3 and b_7 to y_4 . The present technique can be applied to a transfer system or the like for transferring LDPC code for example.

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