

(12) PATENT APPLICATION PUBLICATION

(21) Application No.782/MUM/2013 A

(19) INDIA

(22) Date of filing of Application :14/03/2013

(43) Publication Date : 23/01/2015

(54) Title of the invention : A SYSTEM AND A METHOD TO ENABLE/ DISABLE FACTORY SET FIXED PROTECTION SETTINGS USING SIMULATION KIT

(51) International classification	:G06F13/16, H04B1/16, H04M1/00	(71) <b>Name of Applicant :</b> <b>1)LARSEN &amp; TOUBRO LIMITED</b> Address of Applicant :L & T HOUSE, BALLARD ESTATE, MUMBAI 400 001, STATE OF MAHARASHTRA, INDIA
(31) Priority Document No	:NA	(72) <b>Name of Inventor :</b>
(32) Priority Date	:NA	<b>1)BISHNOI, B. L.;</b>
(33) Name of priority country	:NA	<b>2)LANDE, APEKSHA, B.</b>
(86) International Application No	:NA	<b>3)VEJLANI, ZAINAB</b>
Filing Date	:NA	<b>4)TYAGI, DHRUVI</b>
(87) International Publication No	: NA	
(61) Patent of Addition to Application Number	:NA	
Filing Date	:NA	
(62) Divisional to Application Number	:NA	
Filing Date	:NA	

(57) Abstract :

The present invention relates to a method to Enable / Disable factory set fixed protections. The method comprises storing of the protection state in a non volatile memory means of a microcontroller, sensing by means of said microcontroller the state of the protection from a non volatile memory means and checking of said state of protection adapted to decide whether to check for fault or not to check for fault. The protection state is checked and if it is disabled the said microcontroller does not issue trip even when fault is detected and if the state is enabled said microcontroller issues trip if fault is detected. The invention also relates to a system to Enable / Disable factory set fixed protection settings.

No. of Pages : 18 No. of Claims : 12